# **Boce Lin**

# Ph.D in Electrical Engineering RF/mm-Wave CMOS IC Designer



## boclin@iis.ee.ethz.ch

( +41-76-576-4266

Zürich, Switzerland

/in/bocelineth



English, Chinese

#### **Education**

Ph.D in Electrical **Engineering** ETH Zürich, Switzerland 2025

Master of Science in **Electrical Engineering** Georgia Institute of Technology, USA 2022

Bachelor of Science in **Electrical Engineering Bachelor of Science in Mathematics** Southern Methodist University, USA 2019

### **Skills**

#### **Technology Node**

TSMC FinFET 16nm TSMC CMOS SOI 28nm TSMC CMOS Bulk 65nm GF CMOS SOI 22nm GF CMOS SOI 45nm

#### Software

Cadence Virtuoso **Keysight ADS EMX Ansys HFSS KiCAD MATLAB** 

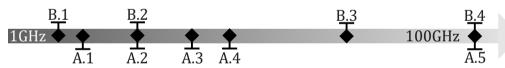
#### **Summary**

PhD in Electrical Engineering with entensive tape-out experience and expertise in RFIC/mm-Wave IC design, including high-frequency circuits like LNA and phased arrays. Proficient in Virtuoso and ADS, eager to contribute technical skills to cutting-edge RFIC development.

# **Working Experience**

- Research Assistant September, 2020 September 2025 Research Assistant in Professor Hua Wang's lab, leading multiple projects on phased array designs and CMOS circuit building blocks, advancing integrated circuits and systems for communication and sensing applications.
- Internship, Yunji Technologies June, 2019 August 2019 Designed and implemented CAN communication modules, and developed embedded systems through research and programming.
- **Teaching Assistant** September, 2017 May 2019 Teaching Assistant for Advanced Filter Design, Electronic Design, and Calculus courses, developing strong skills in effective scientific communication to enhance both teaching and learning experiences.

# **Technical Projects & Research**



#### **Phased Arrays**

- **A.1** Ku-Band SATCOMM Receiver Array (10.7 12.7 GHz)
- **A.2** Ka-Band SATCOMM Receiver Array (17.7 20.7GHz)
- **A.3** 2-D Autonomous Beamformer Receiver Array (23 40GHz)
- **A.4** Dual-Polarization Reiceiver (23 40 GHz)
- **A.5** D-Band 2D Relective Relay Array (115 130GHz)

#### **Circuit Building Blocks**

- **B.1** Cryogenic Low Noise Amplifier (4 8 GHz)
- **B.2** Ka-Band Low Noise Amplifier (15 25 GHz)
- **B.3** V-/E-Band Low Noise Amplifier (50 73 GHz)
- **B.4** D-Band Power Detector (115 130 GHz)

#### Strength

- Analytical Thinking **Tecnical Expertise** 
  - Problem-Solving Attention to Details
- Adaptability and Continuous Leanrning
- Communication and Team work